TO ： 上海億聖

Date :Apr.,28,2019

HannStar Product Specification

(Formal)

7” Color TFT-LCD Module

Model: HSD070JDW6-A00

Note: (1) The information contained herein is tentative and may be changed without prior notices.

(2) Please contact HannStar Display Corp. before designing your product based on this module specification.

(3) The information contained herein is presented merely to indicate the characteristics and performance of our products. No responsibility is assumed by HannStar for any intellectual property claims or other problems that may result from application based on the module described herein.

(4) The mark “ ** ” of Model means sub-model code.

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Record of Revisions

Rev.	Date	Sub-Model	Description of change
1.0	Apr.,28, 2019	A**	Formal Product Specification was first released

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1.0 GENERAL DESCRIPTION 4

2.0 ABSOLUTE MAXIMUM RATINGS 5

3.0 OPTICAL CHARACTERISTICS 6

4.0 BLOCK DIAGRAM 10

5.0 INTERFACE PIN CONNECTION 12

6.0 ELECTRICAL CHARACTERISTICS 15

7.0 RELIABILTY TEST ITEMS 21

8.0 OUTLINE DIMENSION 22

9.0 LOT MARK 24

10.0 PACKAGE SPECIFICATION 25

11.0 GENERAL PRECAUTION 27

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1.0 GENERAL DESCRIPTION

1.1 Introduction

HannStar Display model HSD070JDW6-A is a color active matrix thin film transistor (TFT) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT LCD panel, a driving circuit and a back light system. This TFT LCD has a 7” (15:9) inch diagonally measured active display area with WVGA (800 horizontal by 480 vertical pixel) resolution.

1.2 Features

- 7 (15:9 diagonal) inch configuration
- 8bit , TTL interface
- ROHS / Halogen Free Compliance

1.3 Applications

- Automotive

1.4 General information

Item		Specification	Unit
Outline Dimension		165(H) x 104(V) x 6.8(D)	mm
Display area		152.4 (H) x 91.44 (V)	mm
Number of Pixel		800 RGB (H) x 480(V)	pixels
Pixel pitch		0.1905(H) x 0.1905(V)	mm
Pixel arrangement		RGB Vertical stripe	
Display mode		Normally Black	
NTSC		75 (Typ.)	%
Surface treatment		Antiglare, Hard-Coating (3H)	
Weight		153(Typ.)	g
Power Consumption	Logic System	0.55(Max.)	W
	B/L System	3.808(Max.)	W

1.5 Mechanical Information

Item		Min.	Typ.	Max.	Unit
Module Size	Horizontal (H)	164.7	165	165.3	mm
	Vertical (V)	103.7	104	104.3	mm
	Depth (D)	6.5	6.8	7.1	mm
Weight		143	153	163	g

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2.0 ABSOLUTE MAXIMUM RATINGS

2.1 Electrical Absolute Rating

2.1.1 TFT LCD Module

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	Vcc	-0.3	4.5	V	GND=0
	V_{GH}	-0.3	V_{GL}+35.0	V	GND=0
	V_{GL}	-15	0.3	V	GND=0
	AV_{DD}	-0.3	14.5	V	AGND=0
	V_{COM}	4.0	6.0	V	
Logic Signal Input Level	V_I	-0.3	4.5	V	

Note

(1) Permanent damage may occur to the LCD module if beyond this specification. Functional operation should be restricted to the conditions described under normal operating conditions.

(2) Ta =25±2℃

2.2 Environment Absolute Rating

Item	Symbol	Min.	Max.	Unit	Note
Operating Temperature	T_{opa}	-40	85	℃	
Storage Temperature	T_{stg}	-40	95	℃	

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3.0 OPTICAL CHARACTERISTICS

3.1 Optical specification

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Contrast		CR	Θ=0 Normal viewing angle	700	1000	—		(1)(2)
Response time	Rising	TR+TF		—	30	40	msec	(1)(3)
White luminance (Center)		Y_L		750	1000	—	cd/m^2	(1)(4) (I_L=160mA)
Color chromaticity (CIE1931)	White	W_x		-0.04	0.31	+0.04		(1)(4)
		W_y			0.33			
	Red	R_x			0.650			
		R_y			0.334			
	Green	G_x			0.292			
		G_y			0.611			
	Blue	B_x			0.147			
		B_y			0.061			
Viewing angle	Hor.	Θ_L	CR>10	80	85	—		
		Θ_R		80	85	—		
	Ver.	Θ_U		80	85	—		
		Θ_D		80	85	—		
Brightness uniformity		B_{UNI}	Θ=0	70	80	—	%	(5)
Optima View Direction		Free						(6)

3.2 Measuring Condition

- Measuring surrounding：dark room
- LED current I_L：160mA
- Ambient temperature：25±2ºC
- 15min. warm-up time.

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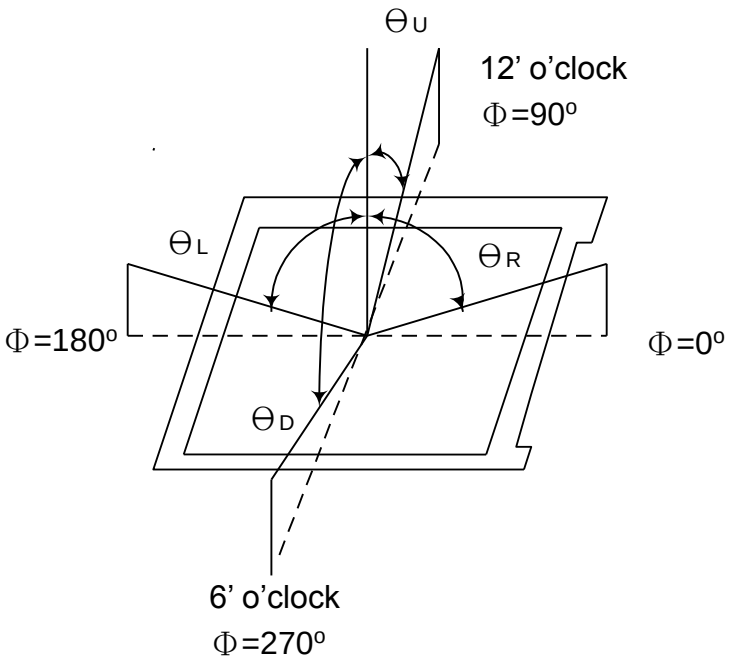
3.3 Measuring Equipment

- FPM520 of Westar Display technologies, INC., which utilized SR-3 for Chromaticity and BM-5A for other optical characteristics.
- Measuring spot size：20 ~ 21 mm

Note (1) Definition of Viewing Angle:



Note (2) Definition of Contrast Ratio (CR) :
measured at the center point of panel

$$CR = \frac{\text{Luminance with all pixels white}}{\text{Luminance with all pixels black}}$$

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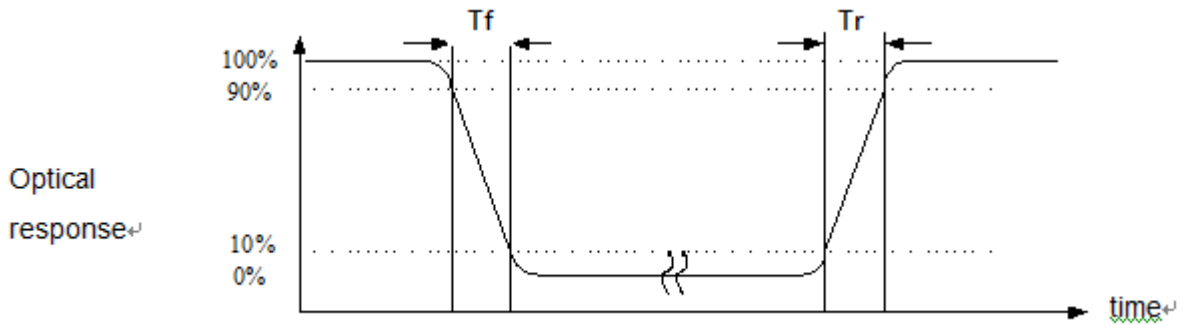
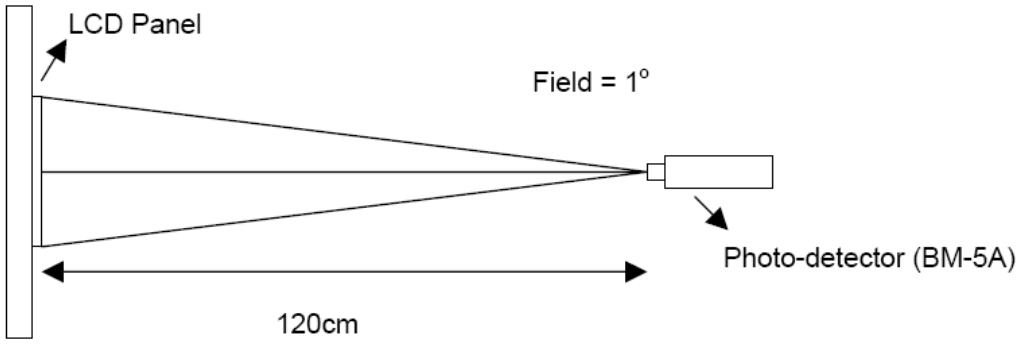
Note (3) Definition of Response Time : Sum of T_R and T_F



Note (4) Definition of optical measurement setup



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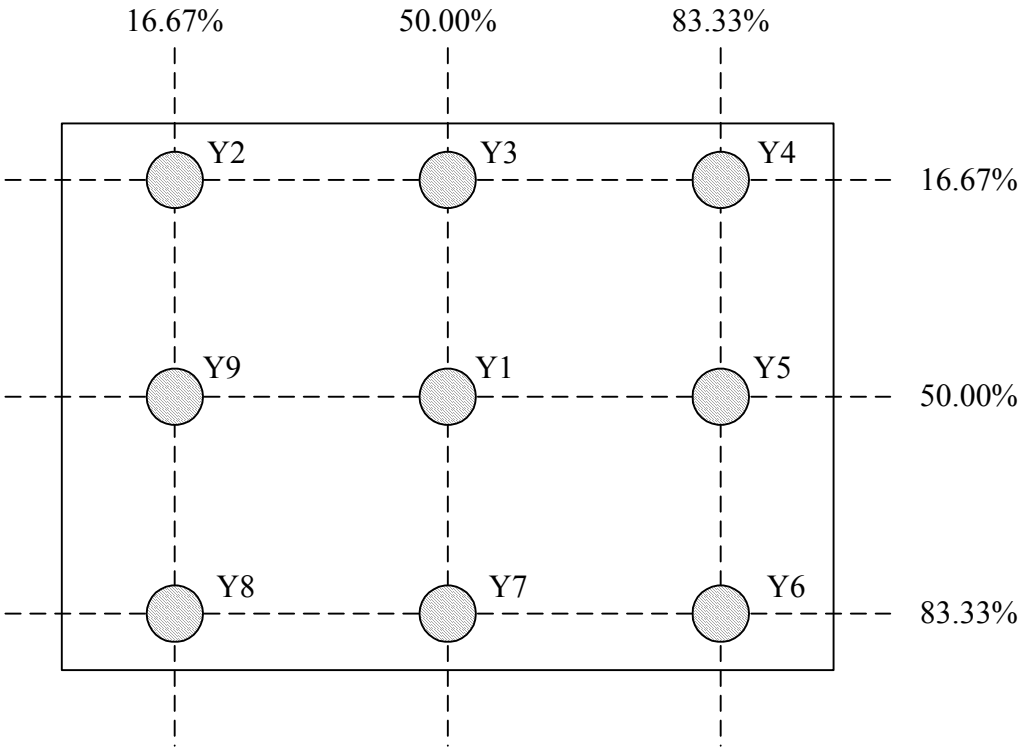
Note (5) Definition of brightness uniformity



$$\text{Luminance uniformity} = \frac{\text{(Min Luminance of 9 points)}}{\text{(Max Luminance of 9 points)}} \times 100\%$$

Note (6)：Rubbing Direction (The different Rubbing Direction will cause the different optima view direction.

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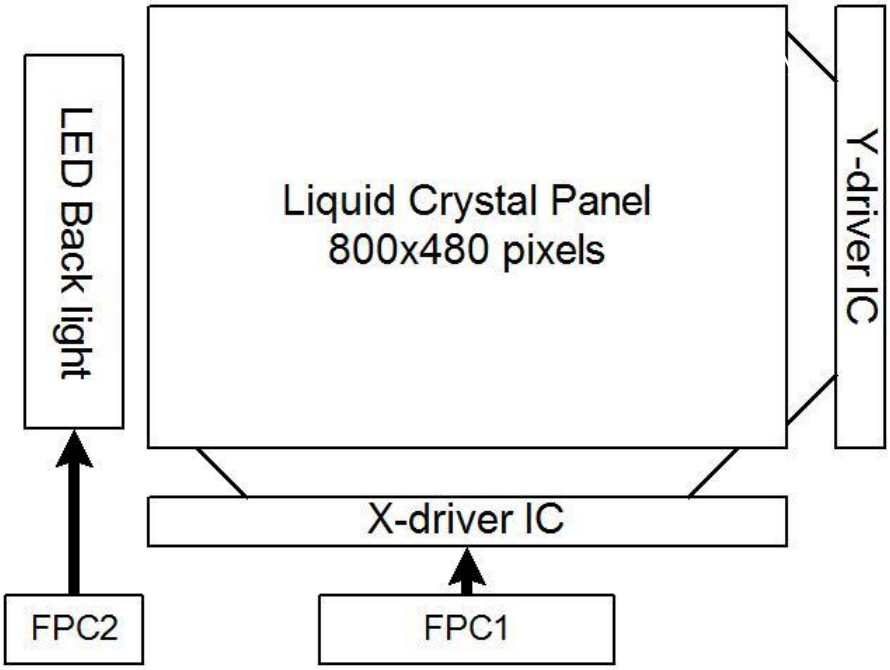
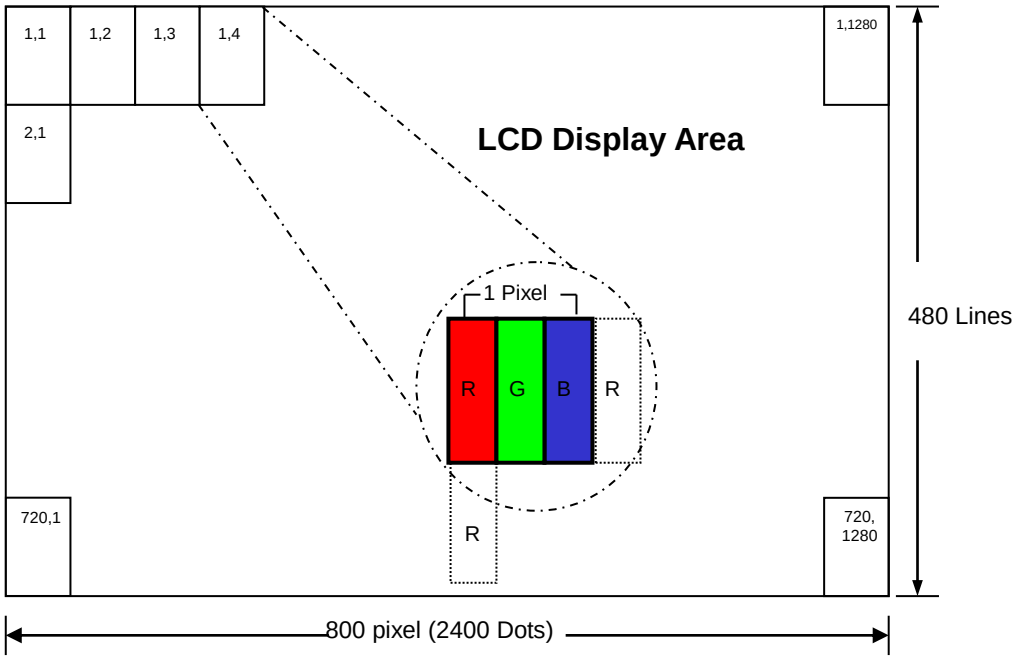
4.0 BLOCK DIAGRAM

4.1 TFT LCD Module:



4.2 Pixel Format



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4.3 Relationship Between Displayed Color and Input

	Display	MSB R7	R6	R5	R4	R3	R2	R1	LSB R0	MSB G7	G6	G5	G4	G3	G2	G1	LSB G0	MSB B7	B6	B5	B4	B3	B2	B1	LSB B0	Gray scale Level
Basic color	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	Light Blue	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	-
	Purple	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	-
	Yellow	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	-
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	-
Gray scale of Red	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L2
	↓	:								:								:								L3…L251
	Light	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L252
		H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L253
		H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L254
	Red	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	Red L255
Gray scale of Green	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	L	L	L2
	↓	:								:								:								L3…L251
	Light	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L	L252
		L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L	L	L	L	L	L	L	L	L253
		L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	L	L254
	Green	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	L	L	L	L	L	L	L	L	Green L255
Gray scale of Blue	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	L	L2
	↓	:								:								:								L3…L251
	Light	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	L	L252
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	L	H	L253
		L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	L	L254
	Blue	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	H	H	H	H	H	H	H	H	Blue L255
Gray scale of White & Black	Black	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L	L0
	Dark	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L1
	↑	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L	L	L	L	L	L	H	L	L2
	↓	:								:								:								L3…L251
	Light	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	H	H	H	H	H	H	L	L	L252
		H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	L253
		H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	H	H	H	H	H	H	H	L	L254
	White	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	H	White L255

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5.0 INTERFACE PIN CONNECTION

5.1 **FPC Pin Assignment:** FPC connector is used for electronics interface. The recommended model is FH52-60S-0.5SH manufactured by Hirose.

Terminal no.	Symbol	I/O	Function
1	AGND	P	Analog Ground
2	AVDD	P	Analog Power
3	V_{CC}	P	Digital Power
4	R0	I	Data Input(LSB)
5	R1	I	Data Input
6	R2	I	Data Input
7	R3	I	Data Input
8	R4	I	Data Input
9	R5	I	Data Input
10	R6	I	Data Input
11	R7	I	Data Input(MSB)
12	G0	I	Data Input(LSB)
13	G1	I	Data Input
14	G2	I	Data Input
15	G3	I	Data Input
16	G4	I	Data Input
17	G5	I	Data Input
18	G6	I	Data Input
19	G7	I	Data Input(MSB)
20	B0	I	Data Input(LSB)
21	B1	I	Data Input
22	B2	I	Data Input
23	B3	I	Data Input
24	B4	I	Data Input
25	B5	I	Data Input
26	B6	I	Data Input
27	B7	I	Data Input(MSB)
28	DCLK	I	Clock input
29	DE	I	Data enable signal
30	HSD	I	Horizontal sync input. Negative polarity
31	VSD	I	Vertical sync input. Negative polarity
32	MODE	I	DE/SYNC mode select .normally pull low H: HV mode L: DE mode
33	RSTB	I	Global reset pin. Normally pull high. H: normal operation. L: the controller is in reset state. Suggest to connecting with an RC (10KΩ, 0.1μF) reset circuit for stability.
34	STBYB	I	Standby mode. Normally pull low. H: normal operation. L: the controller and source driver will turn off. Suggest to connecting with an RC (10KΩ, 0.47μF)reset circuit for stability.
35	SHLR	I	Left or Right Display Control. Note1.

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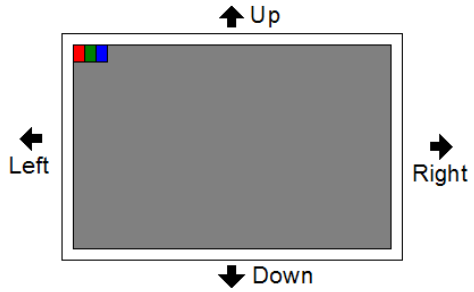
Terminal no.	Symbol	I/O	Function
36	V_{CC}	P	Digital Power
37	UPDN	I	Up / Down Display Control. Note1.
38	GND	P	Digital Ground
39	AGND	P	Analog Ground
40	AVDD	P	Analog Power
41	VCOM	I	For external VCOM DC input
42	N/C	-	Not connect
43	BIST	-	Aging mode on/off control. Please float this pin.
44	N/C	-	Not connect
45	N/C	-	Not connect
46	N/C	-	Not connect
47	N/C	-	Not connect
48	CSB	-	Serial communication chip selection. Please float this pin.
49	SCL	-	Serial communication clock pin. Please float this pin.
50	SDO	-	Serial communication data out pin. Please float this pin.
51	SDI	-	Serial communication data pin. Please float this pin.
52	N/C	-	Not connect
53	VPP_T	-	Power supply for trim function. Please float this pin.
54	N/C	-	Not connect
55	N/C	-	Not connect
56	VGH	P	Positive Power for TFT
57	V_{CC}	P	Digital Power
58	VGL	P	Negative Power for TFT
59	GND	P	Digital Ground
60	N/C	-	Not connect

Note1：SHLR and UPDN control function

SHLR	UPDN	Data shifting
H	H	Left→Right, Up→Down
L	H	Right→Left, Up→Down
L	L	Right→Left, Down→Up
H	L	Left→Right, Down→Up



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5.2 LED FPC Pin Assignment:

(Connector: HIROSE FH19SC-10S-0.5SH)

Terminal no.	Symbol	Function
1	A	Anode
2	A	Anode
3	A	Anode
4	NC	No connect
5	K1	Cathode 1
6	K2	Cathode 2
7	K3	Cathode 3
8	K4	Cathode 4
9	NC	No connect
10	NC	No connect

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6.0 ELECTRICAL CHARACTERISTICS

6.1 TFT LCD Module

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Supply Voltage	Vcc	3.0	3.3	3.6	V	
	VGH	-	19	-	V	
	VGL	-	-10	-	V	
	AVDD	-	12.4	-	V	
VCOM	VCOM	-	5.5	-	V	
Input signal voltage	ViH	0.7*Vcc	-	Vcc	V	Note (1)
	ViL	0	-	0.3*Vcc	V	
Current of power supply	I_{Vcc}	-	15	-	mA	Vcc =3.3V, white pattern
	I_{AVDD}	-	20	-	mA	AVDD=12.5V, white pattern
	I_{VGH}	-	0.25	-	mA	VGH=19V, white pattern
	I_{VGL}	-	0.50	-	mA	VGL=-10V, white pattern

Note (1): HSYNC, VSYNC, DE, Digital Data
Note (2): Be sure to apply the power voltage as the power sequence spec.
Note (3): GND=AGND=0V

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6.2 Interface Timing

Item	Symbol	Min.	Typ.	Max.	Unit	Note
DCLK cycle time	Tcph	20		220	ns	
DCLK pulse duty	Tcwh	35	50	65	%	
VSD setup time	Tvst	8			ns	
VSD hold time	Tvhd	8			ns	
HSD setup time	Thst	8			ns	
HSD hold time	Thhd	8			ns	
Data setup time	Tdsu	8			ns	
Data hold time	Tdhd	8			ns	
DE setup time	Tesu	8			ns	
DE hold time	Tehd	8			ns	
DCLK frequency	fclk	28	30	32	MHz	
Horizontal display area	thd		800		Tcph	
HSD period time	th	899	902	915	Tcph	
HSD pulse width	thpw	5	10	15	Tcph	
HSD back porch	thb		32		Tcph	
HSD front porch	thfp	52	60	68	Tcph	
Vertical display area	tvd		480		th	
VSD period time	tv	546	555	564	th	
VSD pulse width	tvpw	6	10	14	Th	
VSD back porch	tvb		5		th	
VSD front porch	tvfp	55	60	65	th	

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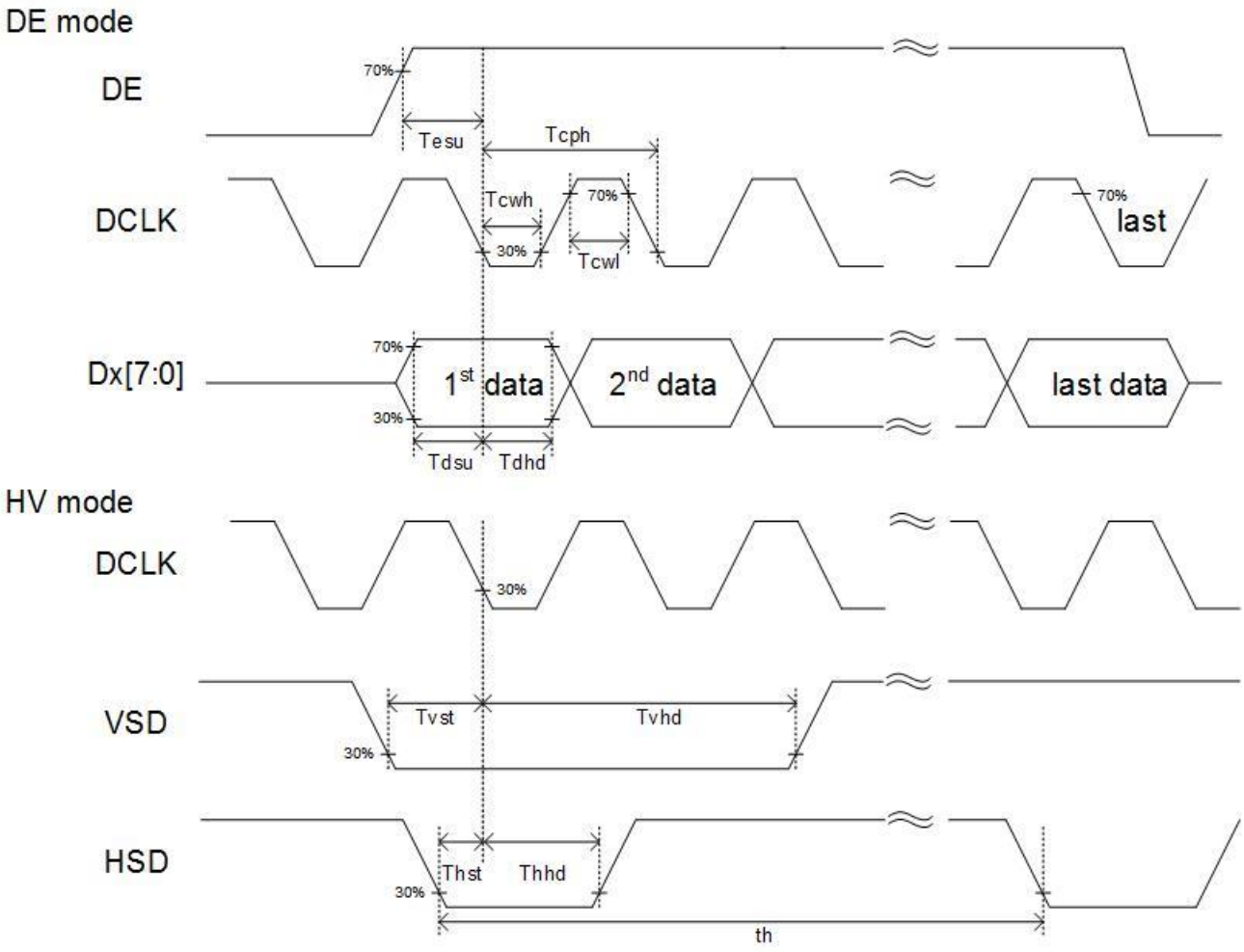
6.3 Timing Diagram of Interface Signal



Input clock and data timing diagram.

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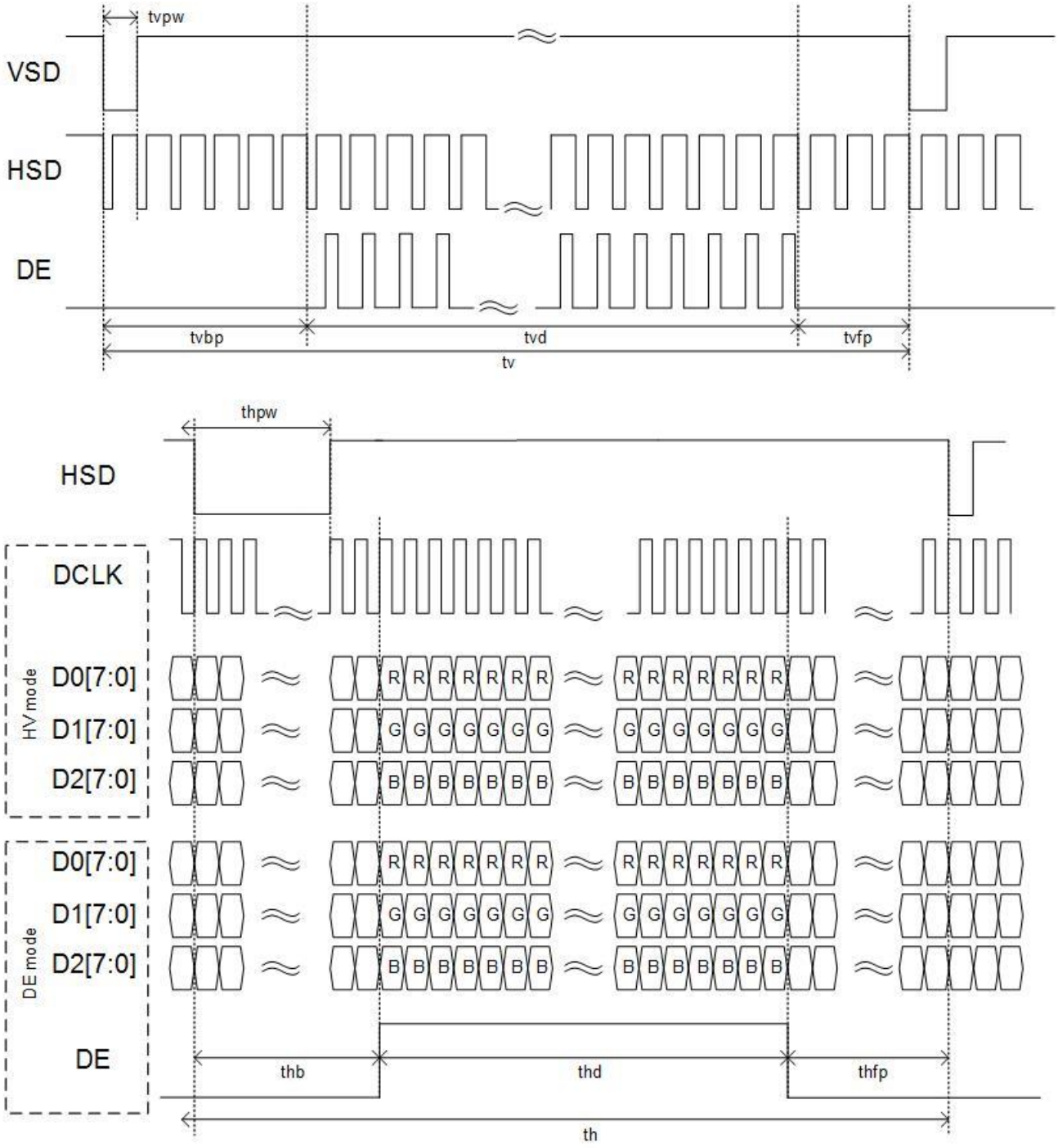

Data Input Format

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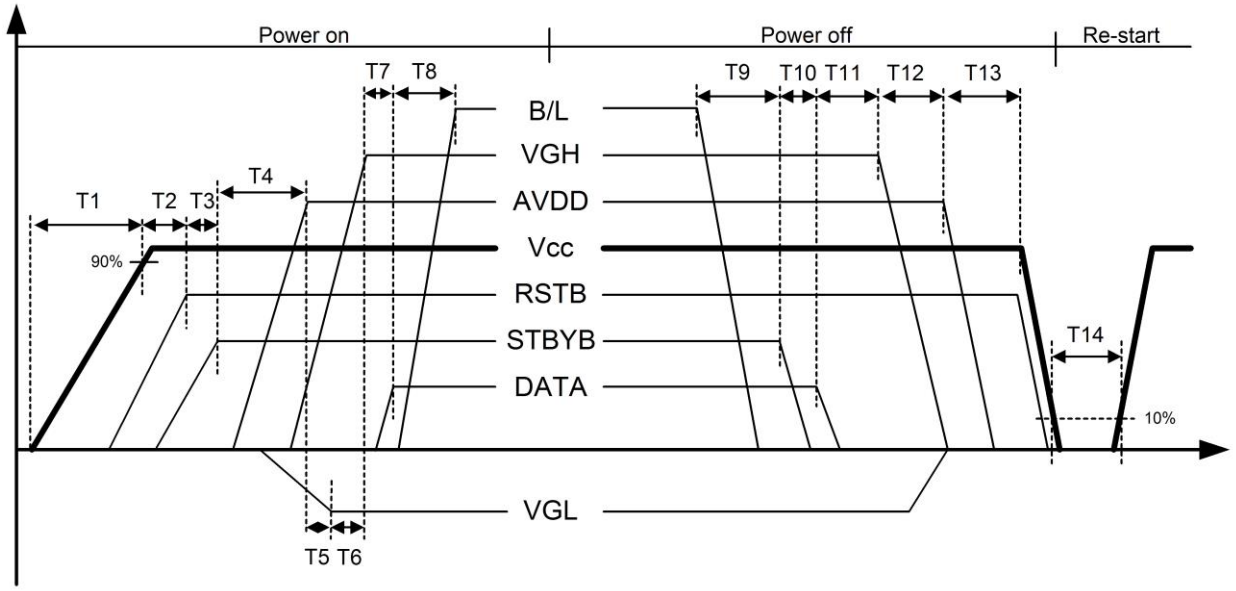
6.4 Power Sequence



Item	Min	Typ.	Max.	Unit
T1	--	--	20	ms
T2	1	--	--	ms
T3	1	--	--	ms
T4	50	--	--	ms
T5	32	--	--	ms
T6	16	--	--	ms
T7	16	--	--	ms
T8	32	--	--	ms
T9	32	--	--	ms
T10	32	--	--	ms
T11	50	--	--	ms
T12	16	--	--	ms
T13	32	--	--	ms
T14	1000	--	--	ms

The Data are included in the R0~R7, G0~G7, B0~B7, HSD, VSD, DCLK, DE, MODE, SHLR, and UPDN.

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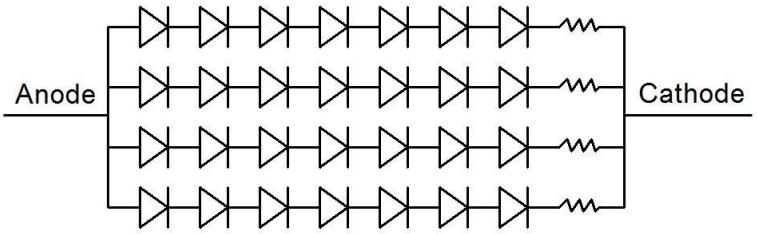
6.5 Backlight Unit

Parameter	Symbol	Min	Typ	Max	Units	Condition
LED Current	I_L	--	160	--	mA	Ta=25℃
LED Voltage	V_F	--	--	23.8	Volt	Ta=25℃
LED Life-Time	N/A	30,000	--	--	Hour	Ta=25℃ Note (2)

Note (1) LED life time (Hr) can be defined as the time in which it continues to operate under the condition: Ta=25±3 ºC, typical IL value indicated in the above table until the brightness becomes less than 50%.

Note (2) The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25℃. and IL=160mA. The LED lifetime could be decreased if operating IL is larger than 160mA. The constant current driving method is suggested.

Note (3) LED Light Bar Circuit



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7.0 RELIABILTY TEST ITEMS

No.	Item	Conditions	Remark
1	High Temperature Storage	Ta=+95℃, 504hrs (1000hrs reference)	Note 1,2,3
2	Low Temperature Storage	Ta=-40℃, 504hrs (1000hrs reference)	Note 1,2,3
3	High Temperature Operation	Ta=+85℃, 504hrs (1000hrs reference)	Note 1,2,3
4	Low Temperature Operation	Ta=-40℃, 504hrs (1000hrs reference)	Note 1,2,3
5	High Temperature and High Humidity (operation)	Ta=+65℃, 90%RH, 504hrs (1000hrs reference)	Note 1,2,3
6	Thermal Cycling Test (non operation)	-40°C(30min) → +85°C(30min), 100 cycles	Note 1,2,3
7	Electrostatic Discharge	Contact = ± 8 kV, class B;(R=330Ω,C=150pF) Air = ± 15 kV, class B;(R=330Ω,C=150pF) 1 time for each point.	
8	Vibration	1.Random: 1.04G, 5~500Hz, XYZ, 30min/each direction 2.Sine: Freq. Range: 8~33.3Hz, Stoke: 1.3mm Sweep: 2.9G, 33.3~400Hz X/Z: 2hrs, Y:4hrs	
9	Shock	Half-Sine, 100G, 6ms, ±XYZ, 1time	
10	Vibration (with carton)	Random: 0.015G^2/Hz, 5~200Hz -6dB/Octave, 200~400Hz XYZ 2hrs/ each direction	
11	Drop (with carton)	Drop height condition, basis on the product weight and follow QB100-0027 1 corner, 3 edges, 6 surfaces	

Note1 : There is no display function NG issue occurred, all the cosmetic specification is judged before the reliability stress.

Note2 : The test result shall be evaluated after the sample has been left at room temperature and humidity for 2 hours without load. No condensation shall be accepted. The sample shall be free from defects: Air bubble in the LCD、Seal leak、Non-display、Missing segments、Glass crack

Note3 : The test condition definition panel's surface temperature.

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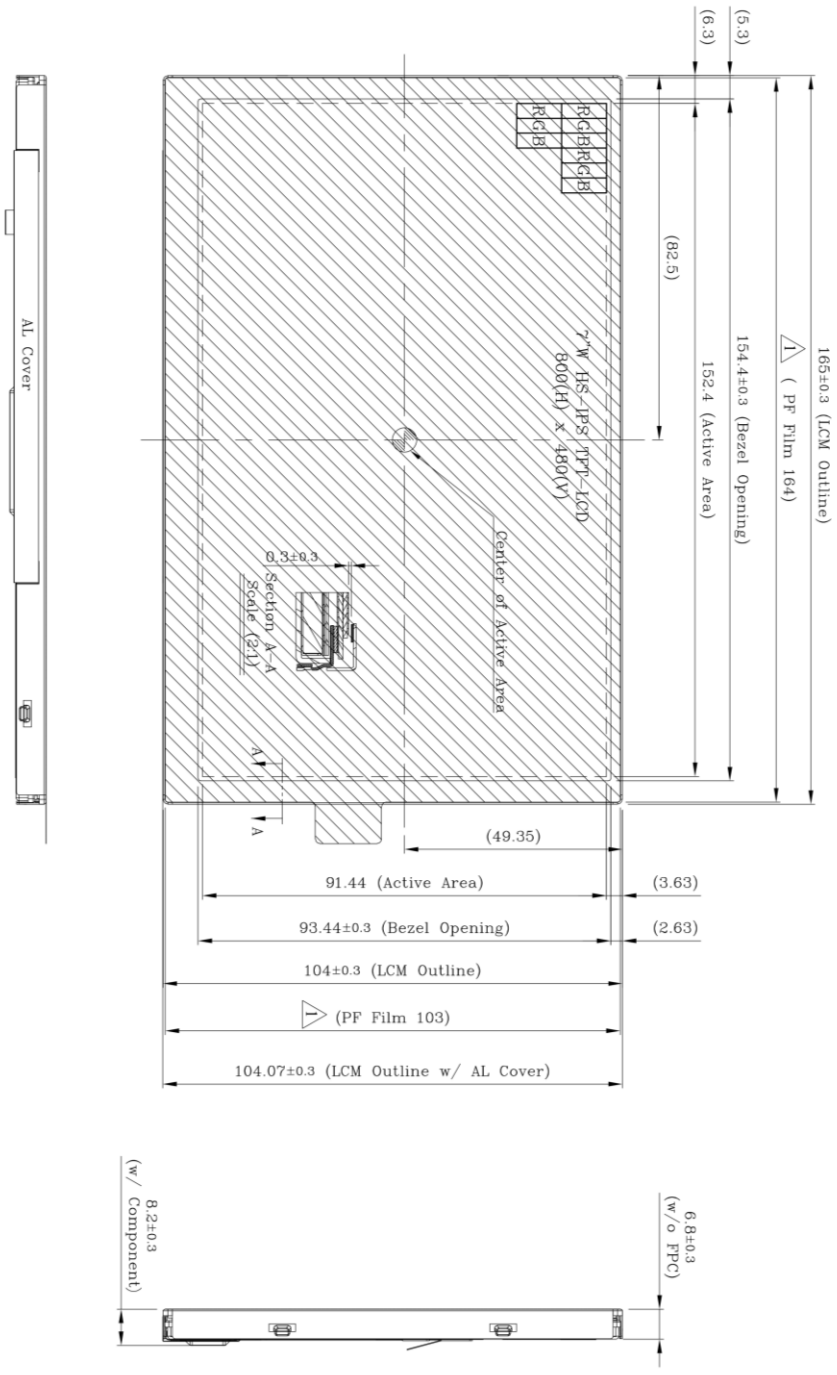
8.0 OUTLINE DIMENSION

8.1 Front View:

Unit : mm



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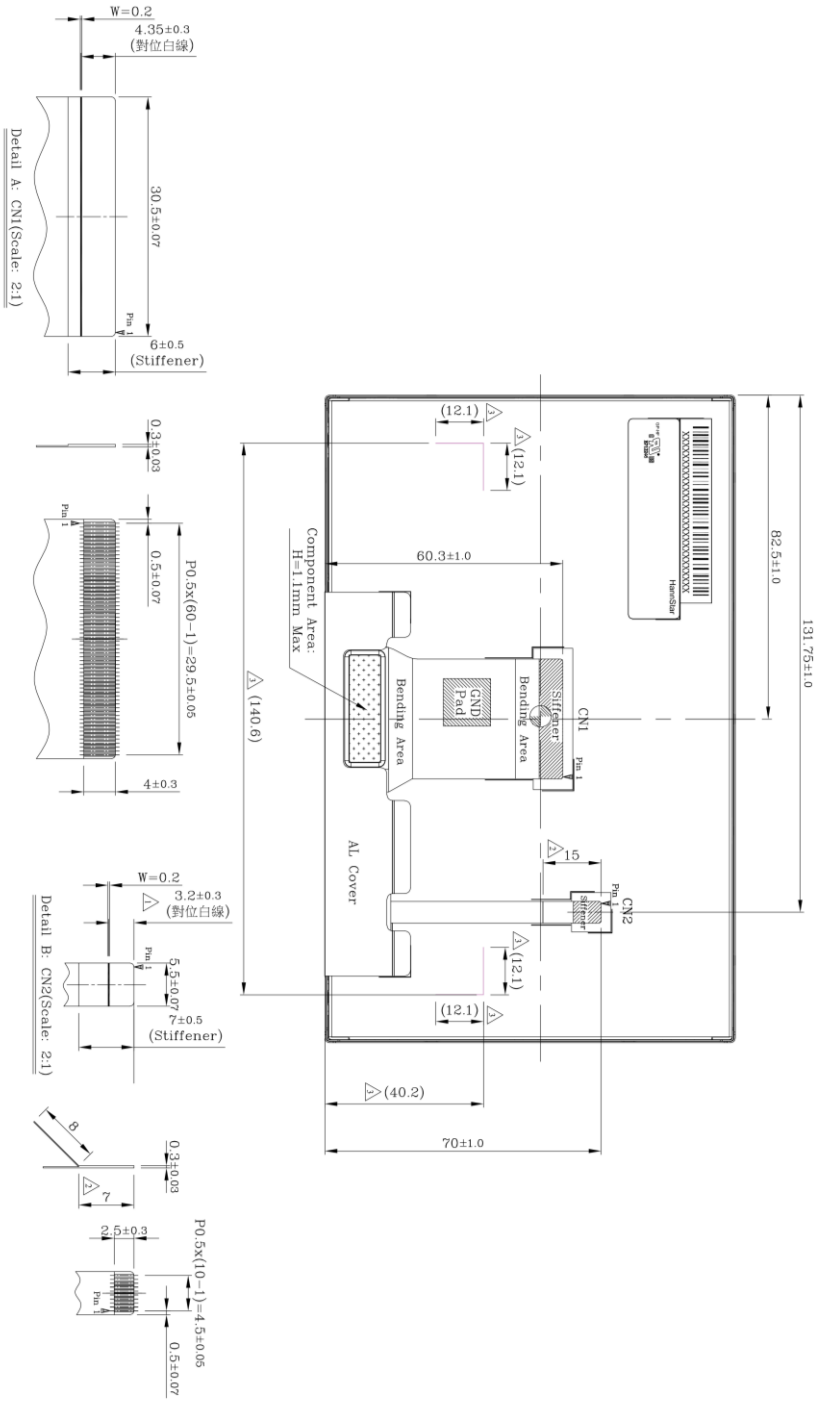
General tolerance:+/-0.3mm

8.2 Rear View:



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General tolerance:+/-0.3mm

9.0 LOT MARK

9.1 Lot Mark

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15

Code 1,2,3,4,5,6: HannStar internal flow control code.
Code 7: production location.
Code 8: production year.
Code 9: production month.
Code 10,11,12,13,14,15: serial number.

Note (1) Production Year: Code 8 is defined by the last number of the year, for example

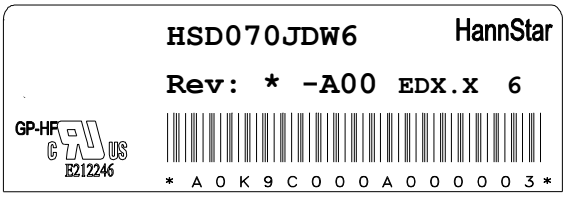
Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026
Mark	6	7	8	9	0	1	2	3	4	5	6

Note (2) Production Month

Month	Jan.	Feb.	Mar.	Apr.	May.	Jun.	Jul.	Aug.	Sep.	Oct	Nov.	Dec.
Mark	1	2	3	4	5	6	7	8	9	A	B	C

9.2 Detail of Lot Mark

(1) Below label is attached on the backside of the LCD module. See Section 8.0: Outline Dimension.
(2) The detail of Lot Mark is attached as below.
(3) This is subject to change without prior notice.



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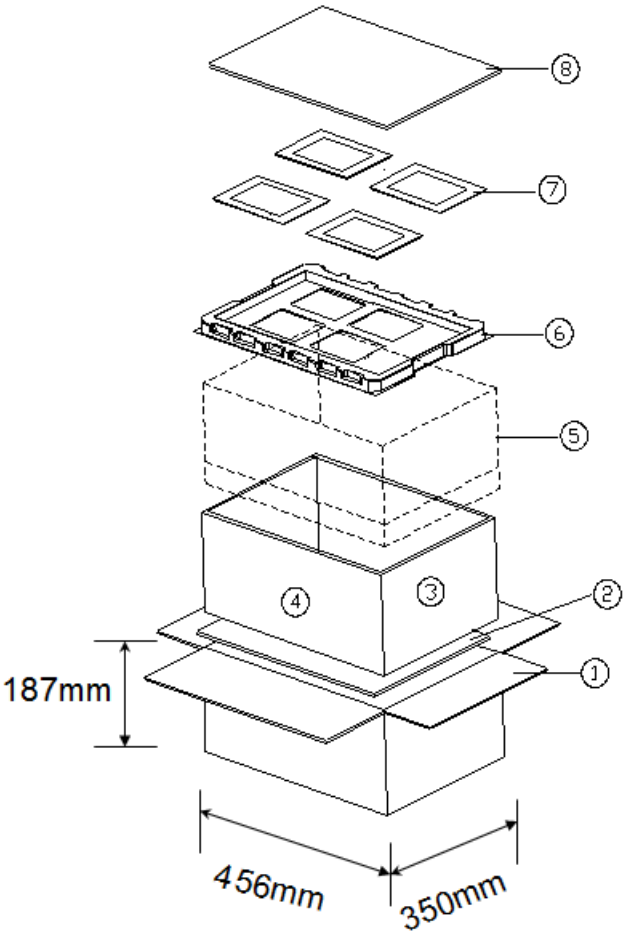
10.0 PACKAGE SPECIFICATION

10.1 Packing form

(1) Package quantity in one carton: 28 pieces.

(2) Carton size: 456mm × 350mm × 187mm



項目	數量	名称
1	1	BOX
2	1	EPE(Down)
3	2	EPE (W)
4	2	EPE(L)
5	1	Antistatic bag
6	7+1	TRAY
7	4pcs/X7=28	MDL
8	1	EPE (TOP)

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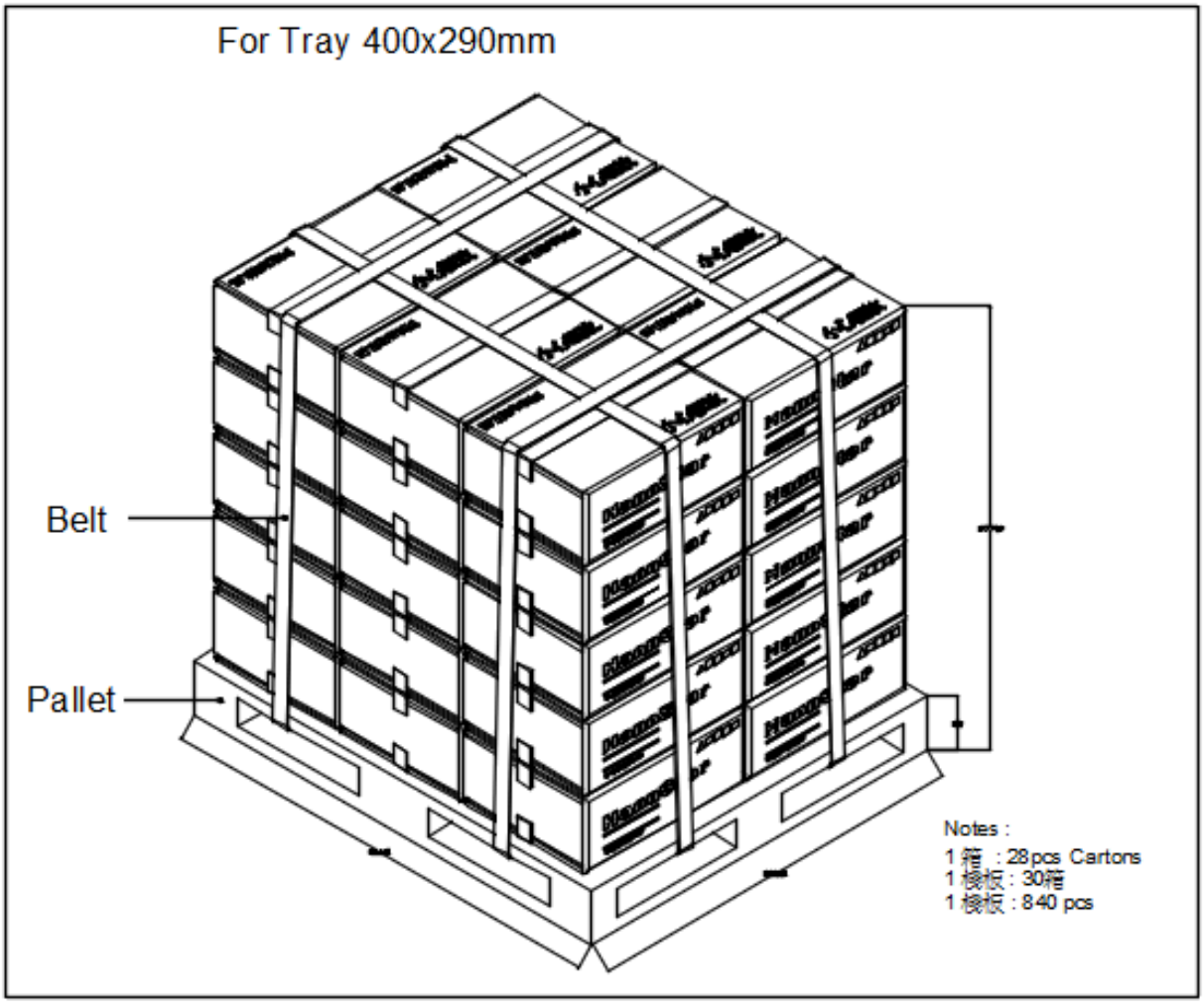
10.2 Pallet Drawing



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11.0 GENERAL PRECAUTION

11.1 Use Restriction

This product is not authorized for use in life supporting systems, aircraft navigation control systems, military systems and any other application where performance failure could be life-threatening or otherwise catastrophic.

11.2 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. HannStar does not warrant the module, if customers disassemble or modify the module.

11.3 Breakage of LCD Panel

11.3.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.

11.3.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.

11.3.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.

11.3.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

11.4 Electric Shock

11.4.1. Disconnect power supply before handling LCD module.

11.4.2. Do not pull or fold the LED cable.

11.4.3. Do not touch the parts inside LCD modules and the fluorescent LED's connector or cables in order to prevent electric shock.

11.5 Absolute Maximum Ratings and Power Protection Circuit

11.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.

11.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.

11.5.3. It's recommended to employ protection circuit for power supply.

11.6 Operation

11.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.

11.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.

11.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.

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11.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

11.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzine or other adequate solvent.

11.7 Mechanism

Please mount LCD module by using mounting holes arranged in four corners tightly.

11.8 Static Electricity

11.8.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

11.8.2 Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge. Please be careful with electrostatic discharge. Persons who handle the module should be grounded through adequate methods.

11.9 Strong Light Exposure

The module shall not be exposed under strong light such as direct sunlight. Otherwise, display characteristics may be changed.

11.10 Disposal

When disposing LCD module, obey the local environmental regulations.

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